EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L5	8	(US-20030088396-$).did. or (US-6819658-$ or US-5623512-$ or US-4737949-$ or US-7162411-$ or US-6813732-$ or US-6598176-$ or US-6145123-$).did.	US-PGPUB; USPAT	OR	OFF	2007/07/27 15:40
L4	0	(collect$ with emulat$ with data) and (arrang$ with (first second) with information with block).clm. and (714/45).ccls.	US-PGPUB; USPAT	OR	ON	2007/07/27 15:36
L3	0	(collect$ with emulat$ with data) and (arrang$ with (first second) with information with block).clm. and (709/232,233,247).ccls.	US-PGPUB; USPAT	OR	ON	2007/07/27 15:36
L2	0	(collect$ with emulat$ with data) and (arrang$ with (first second) with information with block).clm. and (370/394,914).ccls.	US-PGPUB; USPAT	OR	ON	2007/07/27 15:36
S148	0	(collect$ with emulat$ with data) and (arrang$ with (first second) with information with block).clm. and 703/28.ccls.	US-PGPUB; USPAT	OR	ON	2007/07/27 15:35
S150	1	(collect$ with emulat$ with data) and (arrang$ with (first second) with information with block).clm. and (703/26,25,23,28).ccls.	US-PGPUB; USPAT	OR	ON	2007/07/27 14:23
L1	250	703/28.ccls.	US-PGPUB; USPAT	OR	ON	2007/07/27 14:23
S149	2	(collect$ with emulat$ with data) and (arrang$ with (first second) with information with block).clm.	US-PGPUB; USPAT	OR	ON	2007/07/27 11:36
S147	1	"6985848".pn.	US-PGPUB; USPAT	OR	ON	2007/07/27 11:33
S145	282	packet near split$4	USPAT	OR	OFF	2007/07/25 13:10
S144	1235	packet with split$4	USPAT	OR	OFF	2007/07/25 13:10
S143	194	transmission adj rate adj (convert$ adjust$ modif$ shap$)	USPAT	OR	OFF	2007/07/25 12:56
S142	208	(370/230.1).ccls.	USPAT	OR	OFF	2007/07/25 12:55
S141	2	packet adj resiz$	US-PGPUB; USPAT	OR	OFF	2007/07/25 12:38
S140	48	packet with resiz$	US-PGPUB; USPAT	OR	OFF	2007/07/25 12:38

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S139	13	trace with resiz$	US-PGPUB; USPAT	OR	OFF	2007/07/25 12:38

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Inventor Name Search Result

Your Search was:

Last Name = SWOBODA
First Name = GARY

Application#	Patent#	Status	Date Filed	Title	Inventor Name
07718687	Not Issued	163	06/21/1991	PROCESSING SYSTEM, SYSTEM EMULATOR, MASK SETS, AND METHODS	SWOBODA, GARY
08443081	Not Issued	169	05/17/1995	SCAN DESIGN WITH CLOCKING SCHEME TO PERMIT BYPASS OPERATION	SWOBODA, GARY
09738241	6775793	150	12/15/2000	DATA EXCHANGE SYSTEM AND METHOD FOR PROCESSORS	SWOBODA, GARY
09887504	6931636	150	06/22/2001	MULTIPROCESSOR EMULATION SUPPORT USING DYNAMIC LINKING	SWOBODA, GARY
60171392	Not Issued	159	12/21/1999	DATA EXCHANGE SYSTEM AND METHOD FOR PROCESSORS	SWOBODA, GARY
60186339	Not Issued	159	03/02/2000	Wire list walking	SWOBODA, GARY
60223697	Not Issued	159	08/08/2000	Multiprocessor emulation support using dynamic linking	SWOBODA, GARY
60795003	Not Issued	159	04/26/2006	Clock voting	SWOBODA, GARY
09481852	6567933	150	01/14/2000	EMULATION SUSPENSION MODE WITH STOP MODE EXTENSION	SWOBODA, GARY L
11383297	Not Issued	25	05/15/2006	System With Trace Capability Accessed Through the Chip Being Traced	SWOBODA, GARY L
11383300	Not Issued	30	05/15/2006	Using a Delay Line to Cancel Clock Insertion Delays	SWOBODA, GARY L
60747452	Not Issued	159	05/17/2006	BDX and CDX Formats	SWOBODA, GARY L
09273031	Not Issued	161	03/19/1999	PROCESSOR HAVING REAL-TIME EXTERNAL INSTRUCTION INSERTION FOR DEBUG FUNCTIONS WITHOUT A DEBUG MONITOR	SWOBODA, GARY L
60078786	Not Issued	159	03/20/1998	PROCESSOR HAVING REAL-TIME EXTERNAL INSTRUCTION INSERTION FOR DEBUG FUNCTIONS WITHOUT A DEBUG MONITOR	SWOBODA, GARY L
09481853	Not Issued	161	01/14/2000	Emulation system with peripherals recording emulation frame when stop generated	SWOBODA, GARY L.
09483237	6671665	150	01/14/2000	EMULATION SYSTEM WITH SEARCH AND IDENTIFICATION OF OPTIONAL EMULATION PERIPHERALS	SWOBODA, GARY L.
09483321	6836757	150	01/14/2000	EMULATION SYSTEM EMPLOYING SERIAL TEST PORT AND ALTERNATIVE DATA TRANSFER PROTOCOL	SWOBODA, GARY L.
09483367	6553513	150	01/14/2000	EMULATION SUSPEND MODE WITH DIFFERING RESPONSE TO DIFFERING CLASSES OF INTERRUPTS	SWOBODA, GARY L.
09483568	6564339	150	01/14/2000	EMULATION SUSPENSION MODE HANDLING MULTIPLE STOPS AND STARTS	SWOBODA, GARY L.
09483570	6820051	150	01/14/2000	SOFTWARE EMULATION MONITOR EMPLOYED WITH HARDWARE SUSPEND MODE	SWOBODA, GARY L.
09483697	6557116	150	01/14/2000	EMULATION SUSPENSION MODE WITH FRAME CONTROLLED RESOURCE ACCESS	SWOBODA, GARY L.
09740868	6754599	150	12/21/2000	DEBUG OUTPUT LOOSELY COUPLED WITH PROCESSOR BLOCK	SWOBODA, GARY L.
09740917	6868376	150	12/19/2000	DEBUG BI-PHASE EXPORT AND DATA RECOVERY	SWOBODA, GARY L.
09740921	6725391	150	12/19/2000	CLOCK MODES FOR A DEBUG PORT WITH ON THE FLY CLOCK SWITCHING	SWOBODA, GARY L.

09741645	6388533	150	12/19/2000	Programmable ring oscillator	SWOBODA, GARY L.
09741647	6545549	150	12/19/2000	REMOTELY CONTROLLABLE PHASE LOCKED LOOP CLOCK CIRCUIT	SWOBODA, GARY L.
09798001	6708290	150	03/02/2001	CONFIGURABLE DEBUG SYSTEM WITH WIRE LIST WALKING	SWOBODA, GARY L.
09798173	6785850	150	03/02/2001	SYSTEM AND METHOD FOR AUTOMATICALLY CONFIGURING A DEBUG SYSTEM	SWOBODA, GARY L.
09798365	6928403	150	03/02/2001	AUTOMATIC DETECTION OF CONNECTIVITY BETWEEN AN EMULATOR AND A TARGET DEVICE	SWOBODA, GARY L.
09798425	6836882	150	03/02/2001	PIPELINE FLATTENER FOR SIMPLIFYING EVENT DETECTION DURING DATA PROCESSOR DEBUG OPERATIONS	SWOBODA, GARY L.
09798429	6754852	150	03/02/2001	DEBUG TRIGGER BUILDER	SWOBODA, GARY L.
09798555	7113902	150	03/02/2001	DATA PROCESSING CONDITION DETECTOR WITH TABLE LOOKUP	SWOBODA, GARY L.
09798595	6947884	150	03/02/2001	SCAN INTERFACE WITH TDM FEATURE FOR PERMITTING SIGNAL OVERLAY	SWOBODA, GARY L.
09798596	6859897	150	03/02/2001	RANGE BASED DETECTION OF MEMORY ACCESS	SWOBODA, GARY L.
09798606	6738929	150	03/02/2001	DYNAMICALLY CONFIGURABLE DEBUG PORT FOR CONCURRENT SUPPORT OF DEBUG FUNCTIONS FROM MULTIPLE DATA PROCESSING CORES	SWOBODA, GARY L.
09920180	7089437	150	08/01/2001	APPARATUS FOR DETERMINING POWER CONSUMED BY A BUS OF A DIGITAL SIGNAL PROCESSOR USING CONUNTED NUMBER OF LOGIC STATE TRANSITIONS ON BUS	SWOBODA, GARY L.
09920193	Not Issued	161	08/01/2001	Apparatus and method for central processing unit power measurement in a digital signal processor	SWOBODA, GARY L.
09924800	6820222	150	08/08/2001	APPARATUS AND METHOD FOR PROCESSOR POWER MEASUREMENT IN A DIGITAL SIGNAL PROCESSOR USING TRACE DATA AND SIMULATION TECHNIQUES	SWOBODA, GARY L.
09924912	6795879	150	08/08/2001	APPARATUS AND METHOD FOR WAIT STATE ANALYSIS IN A DIGITAL SIGNAL PROCESSING SYSTEM	SWOBODA, GARY L.
09938201	6539497	150	08/22/2001	IC WITH SELECTIVELY APPLIED FUNCTIONAL AND TEST CLOCKS	SWOBODA, GARY L.
09943137	6912675	150	08/30/2001	USING SELECTIVE OMISSION TO COMPRESS ON-CHIP DATA PROCESSOR TRACE AND TIMING INFORMATION FOR EXPORT	SWOBODA, GARY L.
09943456	7206734	150	08/30/2001	EXPORTING ON-CHIP DATA PROCESSOR TRACE INFORMATION WITH VARIABLE PROPORTIONS OF CONTROL AND DATA	SWOBODA, GARY L.
09943595	Not Issued	71	08/30/2001	Collecting and exporting on-chip data processor trace and timing information with differing collection and export formats	SWOBODA, GARY L.
09943598	7043418	150	08/30/2001	SYNCHRONIZING ON-CHIP DATA PROCESSOR TRACE AND TIMING INFORMATION FOR EXPORT	SWOBODA, GARY L.
09943599	Not Issued	124	08/30/2001	Correlating on-chip data processor trace information for export	SWOBODA, GARY L.
09943603	7076419	150	08/30/2001	USING SIGN EXTENSION TO COMPRESS ON-CHIP DATA PROCESSOR TRACE AND TIMING INFORMATION FOR EXPORT	SWOBODA, GARY L.
09949265	7020600	150	09/07/2001	APPARATUS AND METHOD FOR IMPROVEMENT OF COMMUNICATION BETWEEN AN EMULATOR UNIT AND A HOST DEVICE	SWOBODA, GARY L.
10212559	Not Issued	161	08/05/2002	Apparatus and method for a reversible emulator/target cable connector	SWOBODA, GARY L.
10212621	6865504	150	08/05/2002	APPARATUS AND METHOD FOR A RECONFIGURABLE POD INTERFACE FOR USE WITH AN EMULATOR UNIT	SWOBODA, GARY L.

10212622	Not Issued	164	08/05/2002	APPARATUS AND METHOD FOR DEVICE SELECTIVE SCANS IN DATA STREAMING TEST ENVIRONMENT FOR A PROCESSING UNIT HAVING MULTIPLE CORES	SWOBODA, GARY L.

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Last Name = SWOBODA
First Name = GARY

Application#	Patent#	Status	Date Filed	Title	Inventor Name
10241420	Not Issued	160	09/11/2002	Apparatus and method for an on-board trace recorder unit	SWOBODA, GARY L.
10256373	Not Issued	164	09/27/2002	APPARATUS AND METHOD FOR A TRACE SYSTEM ON A CHIP HAVING MULTIPLE PROCESSING UNITS	SWOBODA, GARY L.
10301894	6948155	150	11/22/2002	LITTLE OFFSET IN MULTICYCLE EVENT MAINTAINING CYCLE ACCURATE TRACING OF STOP EVENTS	SWOBODA, GARY L.
10301935	Not Issued	71	11/22/2002	Data trace compression map	SWOBODA, GARY L.
10302025	7047451	150	11/22/2002	TRACING PROGRAM COUNTER ADDRESSES USING NATIVE PROGRAM COUNTER FORMAT AND INSTRUCTION COUNT FORMAT	SWOBODA, GARY L.
10302449	6981178	150	11/22/2002	SEPARATION OF DEBUG WINDOWS BY IDS BIT	SWOBODA, GARY L.
10336986	6760866	150	01/06/2003	PROCESS OF OPERATING A PROCESSOR WITH DOMAINS AND CLOCKS	SWOBODA, GARY L.
10337568	6996747	150	01/07/2003	PROGRAM COUNTER TRACE STACK, ACCESS PORT, AND SERIAL SCAN PATH	SWOBODA, GARY L.
10728627	Not Issued	161	12/05/2003	Apparatus and method for synchronization of trace streams from multiple processors	SWOBODA, GARY L.
10729190	Not Issued	161	12/05/2003	Apparatus and method for trace stream identification of a pause point in code execution sequence	SWOBODA, GARY L.
10729191	Not Issued	30	12/05/2003	Apparatus and method for op code extension in packet groups transmitted in trace streams	SWOBODA, GARY L.
10729196	Not Issued	41	12/05/2003	Apparatus and method for compression of the timing trace stream	SWOBODA, GARY L.
10729212	Not Issued	41	12/05/2003	Apparatus and method for separating detection and assertion of a trigger event	SWOBODA, GARY L.
10729214	Not Issued	164	12/05/2003	APPARATUS AND METHOD FOR TRACE STREAM IDENTIFICATION OF A PROCESSOR DEBUG HALT SIGNAL	SWOBODA, GARY L.
10729239	Not Issued	161	12/05/2003	Apparatus and method for state selectable trace stream generation	SWOBODA, GARY L.
10729272	Not Issued	164	12/05/2003	APPARATUS AND METHOD FOR TRACE STREAM IDENTIFICATION OF MULTIPLE TARGET PROCESSOR EVENTS	SWOBODA, GARY L.
10729326	7225365	150	12/05/2003	APPARATUS AND METHOD FOR IDENTIFICATION OF A NEW SECONDARY CODE START POINT FOLLOWING A RETURN FROM A SECONDARY CODE EXECUTION	SWOBODA, GARY L.
10729327	7210072	150	12/05/2003	APPARATUS AND METHOD FOR TRACE STREAM IDENTIFICATION OF A PIPELINE FLATTENER PRIMARY CODE FLUSH FOLLOWING INITIATION OF AN INTERRUPT SERVICE ROUTINE	SWOBODA, GARY L.
10729400	Not Issued	161	12/05/2003	Apparatus and method for capturing the program counter address associated with a trigger signal in a target processor	SWOBODA, GARY L.
10729401	Not Issued	161	12/05/2003	Apparatus and method for identification of a primary code start sync point following a return to primary code execution	SWOBODA, GARY L.
10729407	Not Issued	161	12/05/2003	Apparatus and method for a flush procedure in an interrupted trace stream	SWOBODA, GARY L.
10729564	Not Issued	161	12/05/2003	Apparatus and method for capturing an event or combination	SWOBODA, GARY L.

				of events resulting in a trigger signal in a target processor	
10729591	Not Issued	164	12/05/2003	APPARATUS AND METHOD FOR REPORTING PROGRAM HALTS IN AN UNPROTECTED PIPELINE AT NON-INTERRUPTIBLE POINTS IN CODE EXECUTION	SWOBODA, GARY L.
10729592	Not Issued	90	12/05/2003	APPARATUS AND METHOD FOR DETECTING ADDRESS CHARACTERISTICS FOR USE WITH A TRIGGER GENERATION UNIT IN A TARGET PROCESSOR	SWOBODA, GARY L.
10729639	7237151	150	12/05/2003	APPARATUS AND METHOD FOR TRACE STREAM IDENTIFICATION OF A PROCESSOR RESET	SWOBODA, GARY L.
10729647	Not Issued	164	12/05/2003	APPARATUS AND METHOD FOR TRACE STREAM IDENTIFICATION OF A PIPELINE FLATTENER SECONDARY CODE FLUSH FOLLOWING A RETURN TO PRIMARY CODE EXECUTION	SWOBODA, GARY L.
10729650	Not Issued	164	12/05/2003	APPARATUS AND METHOD FOR SELECTING PROGRAM HALTS IN AN UNPROTECTED PIPELINE AT NON-INTERRUPTIBLE POINTS IN CODE EXECUTION	SWOBODA, GARY L.
10771650	Not Issued	41	02/02/2004	Emulation devices, systems and methods utilizing state machines	SWOBODA, GARY L.
10882476	Not Issued	161	07/01/2004	Devices, systems and methods for mode driven stops notice	SWOBODA, GARY L.
11064242	Not Issued	30	02/22/2005	Emulation system with peripherals recording emulation frame when stop generated	SWOBODA, GARY L.
11080698	Not Issued	61	03/15/2005	Apparatus and method for exchanging non-JTAG signals with a core processor during selected JTAG modes	SWOBODA, GARY L.
11080699	Not Issued	30	03/15/2005	Apparatus and method for providing system and test clock signals to an integrated circuit on a single pin	SWOBODA, GARY L.
11080701	Not Issued	41	03/15/2005	Apparatus and method for eliminating the TMS connection in a JTAG procedure	SWOBODA, GARY L.
11127946	Not Issued	30	05/11/2005	Processor condition sensing circuits, systems and methods	SWOBODA, GARY L.
11292597	Not Issued	30	12/02/2005	Multiple test access port protocols sharing common signals	SWOBODA, GARY L.
11292598	Not Issued	25	12/02/2005	Using zero bit scan as a control event	SWOBODA, GARY L.
11292703	Not Issued	25	12/02/2005	Conveying state data through state transitions and number of stays in states	SWOBODA, GARY L.
11293067	Not Issued	25	12/02/2005	BDX data in stable states	SWOBODA, GARY L.
11293599	Not Issued	25	12/02/2005	Locating and labeling device in a multi drop configuration	SWOBODA, GARY L.
11351443	Not Issued	30	02/09/2006	System and method for sharing a communications link between multiple communications protocols	SWOBODA, GARY L.
11381688	Not Issued	25	05/04/2006	Systems And Methods For Multiplexing And Demultiplexing Multiple Data Sources	SWOBODA, GARY L.
11382140	Not Issued	30	05/08/2006	Synchronizing On-Chip Data Processor Trace and Timing Information for Export	SWOBODA, GARY L.
11382835	Not Issued	93	05/11/2006	HIGH SPEED DATA RECORDING WITH INPUT DUTY CYCLE DISTORTION	SWOBODA, GARY L.
11383210	Not Issued	30	05/14/2006	TRANSFERRING CONTROL BETWEEN PROGRAMS OF DIFFERENT SECURITY LEVELS	SWOBODA, GARY L.
11383212	Not Issued	30	05/14/2006	AUTOMATIC HALTING OF A PROCESSOR IN DEBUG MODE DUE TO RESET	SWOBODA, GARY L.
11383290	7209058	150	05/15/2006	TRACE RECEIVER DATA COMPRESSION	SWOBODA, GARY L.
11383294	Not Issued	30	05/15/2006	Multi-Port Trace Receiver	SWOBODA, GARY L.
11383303	Not Issued	30	05/15/2006	TOD or Time Stamp Inserted Into Trace Recording Stream	SWOBODA, GARY L.
11383305	Not Issued	30	05/15/2006	Scaled Time Trace	SWOBODA, GARY L.
11383310	Not Issued	30	05/15/2006	Distributed Width Trace Receiver	SWOBODA, GARY L.

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Your Search was:

Last Name = SWOBODA
First Name = GARY

Application#	Patent#	Status	Date Filed	Title	Inventor Name
11383313	Not Issued	30	05/15/2006	Distributed Depth Trace Receiver	SWOBODA, GARY L.
11383331	Not Issued	30	05/15/2006	Recording Control Point in Trace Receivers	SWOBODA, GARY L.
11383332	Not Issued	25	05/15/2006	Debug Tool Communication Through a Tool to Tool Connection	SWOBODA, GARY L.
11383337	Not Issued	20	05/15/2006	Tracing Program Counter Addresses Using Native Program Counter Format and Instruction Count Format	SWOBODA, GARY L.
11383349	Not Issued	30	05/15/2006	WRITING TO A SPECIFIED CACHE	SWOBODA, GARY L.
11383361	Not Issued	30	05/15/2006	REAL-TIME MONITORING, ALIGNMENT, AND TRANSLATION OF CPU STALLS OR EVENTS	SWOBODA, GARY L.
11383370	Not Issued	30	05/15/2006	PROFILING SYSTEM	SWOBODA, GARY L.
11383374	Not Issued	30	05/15/2006	BYPASSING CACHE INFORMATION	SWOBODA, GARY L.
11383379	Not Issued	30	05/15/2006	VISUALIZING CONTENTS AND STATES OF HIERARCHICAL STORAGE SYSTEMS ACROSS MULTIPLE CORES	SWOBODA, GARY L.
11383385	Not Issued	30	05/15/2006	DETERMINING DIFFERENCES BETWEEN CACHED COPIES OF AN ADDRESS	SWOBODA, GARY L.
11383389	Not Issued	30	05/15/2006	EVENT AND STALL SELECTION	SWOBODA, GARY L.
11383424	Not Issued	30	05/15/2006	METHOD AND SYSTEM OF IDENTIFYING OVERLAYS USED BY A PROGRAM	SWOBODA, GARY L.
11383433	Not Issued	30	05/15/2006	EVENT-GENERATING INSTRUCTIONS	SWOBODA, GARY L.
11383438	Not Issued	30	05/15/2006	SELECTIVELY EMBEDDING EVENT-GENERATING INSTRUCTIONS	SWOBODA, GARY L.
11383442	Not Issued	30	05/15/2006	EVENT AND STALL SELECTION	SWOBODA, GARY L.
11383448	Not Issued	25	05/15/2006	DISPLAYING CACHE INFORMATION USING MARK-UP TECHNIQUES	SWOBODA, GARY L.
11383454	Not Issued	30	05/15/2006	PRIORITIZING CACHES HAVING A COMMON CACHE LEVEL	SWOBODA, GARY L.
11383459	Not Issued	30	05/15/2006	PROVIDING INFORMATION ASSOCIATED WITH A CACHE	SWOBODA, GARY L.
11383461	Not Issued	30	05/15/2006	PROVIDING CACHE STATUS INFORMATION ACROSS MULTIPLE CACHE LEVELS	SWOBODA, GARY L.
11383462	Not Issued	30	05/15/2006	VISUALIZING CONTENTS AND STATES OF HIERARCHICAL STORAGE SYSTEMS	SWOBODA, GARY L.
11383464	Not Issued	30	05/15/2006	WATERMARK COUNTER WITH RELOAD REGISTER	SWOBODA, GARY L.
11383466	Not Issued	30	05/15/2006	METHOD OF TRANSLATING SYSTEM EVENTS INTO SIGNALS FOR ACTIVITY MONITORING	SWOBODA, GARY L.
11383468	Not Issued	30	05/15/2006	METHOD AND SYSTEM OF IDENTIFYING OVERLAYS	SWOBODA, GARY L.
11383469	Not Issued	30	05/15/2006	METHOD AND SYSTEM OF INSERTING MARKING VALUES USED TO CORRELATE TRACE DATA AS BETWEEN PROCESSOR CODES	SWOBODA, GARY L.
11383472	Not Issued	30	05/15/2006	SYSTEMS AND METHODS FOR STALL MONITORING	SWOBODA, GARY L.
11383473	Not Issued	30	05/15/2006	MONITORING OF MEMORY AND EXTERNAL EVENTS	SWOBODA, GARY L.
11383474	Not Issued	25	05/15/2006	NAVIGATING TRACE DATA	SWOBODA, GARY L.

11383475	Not Issued	30	05/15/2006	SYSTEMS AND METHODS FOR CONTROLLING ACCESS TO SECURE DEBUGGING AND PROFILING FEATURES OF A COMPUTER SYSTEM	SWOBODA, GARY L.
11383541	Not Issued	30	05/16/2006	Using A Chip as a Simulation Engine	SWOBODA, GARY L.
11383543	Not Issued	25	05/16/2006	Behavior of Trace in Non-Emulatable Code	SWOBODA, GARY L.
11383600	Not Issued	30	05/16/2006	Paced Trace Transmission	SWOBODA, GARY L.
11383609	Not Issued	25	05/16/2006	Debug Event Instruction	SWOBODA, GARY L.
11383612	Not Issued	30	05/16/2006	Tracing sources with export routing information provided by the source	SWOBODA, GARY L.
11383614	Not Issued	25	05/16/2006	Embedding Event Information in the Timing Stream	SWOBODA, GARY L.
11422617	Not Issued	25	06/07/2006	Data Processing Condition Detector With Table Lookup	SWOBODA, GARY L.
11423702	Not Issued	25	06/12/2006	SYSTEM AND METHOD FOR IMPROVED PERFORMANCE AND OPTIMIZATION OF DATA EXCHANGES OVER A COMMUNICATIONS LINK	SWOBODA, GARY L.
11458841	Not Issued	30	07/20/2006	Auxiliary Link Control Commands	SWOBODA, GARY L.
11711437	Not Issued	20	02/27/2007	Emulation export sequence with distributed control	SWOBODA, GARY L.
60186324	Not Issued	159	03/02/2000	Dynamic query	SWOBODA, GARY L.
60186326	Not Issued	159	03/02/2000	System and method for controlling system hardware	SWOBODA, GARY L.
60186335	Not Issued	159	03/02/2000	One to one correspondence between software and hardware	SWOBODA, GARY L.
60186337	Not Issued	159	03/02/2000	Resource reservation based on query	SWOBODA, GARY L.
60186338	Not Issued	159	03/02/2000	Distributed intelligence	SWOBODA, GARY L.
60186343	Not Issued	159	03/02/2000	Database Generation	SWOBODA, GARY L.
60219340	Not Issued	159	03/02/2000	Emulation, debug and simulation for microprocessor	SWOBODA, GARY L.
60299016	Not Issued	159	06/18/2001	Bus power measurement in a digital signal processor	SWOBODA, GARY L.
60299023	Not Issued	159	06/18/2001	CPU power measurement in a digital signal processor	SWOBODA, GARY L.
60327891	Not Issued	159	10/09/2001	Apparatus and method for an on-board trace recorder unit	SWOBODA, GARY L.
60327903	Not Issued	159	10/09/2001	Apparatus and method for automatic test data transfer compensation	SWOBODA, GARY L.
60327912	Not Issued	159	10/09/2001	Apparatus and method for measuring the power consumption parameter in a target unit by an emulator unit	SWOBODA, GARY L.

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